

## Important Dates

**June 30, 2022**

Digest submission  
deadline

**Sept. 30, 2022**

Notification of  
acceptance

**October 20, 2022**

Final paper submission



## Call for Papers

Hosted by



The University of Fiji



The 7<sup>th</sup> IEEE Southern Power Electronics Conference, SPEC 2022, offers an ideal opportunity for researchers, engineers, academics, and students from all over the world to bring the latest technological advances and applications in Power Electronics to the Southern Hemisphere, as well as to network and promote the discipline.

Prominent researchers in this field will present keynote speeches during the four-day programme that also features tutorials and technical sessions on theory, analysis, design, testing and advances within the field of power electronics.

The conference venue, Sheraton Resort, is situated in the vibrant city of Denarau Island - the commercial city of Fiji. Conference delegates can anticipate a beautiful 'clean & green' landscape and a social programme that includes exciting outdoor activities, scrumptious food and fine Fijian hospitality. IEEE PELS student travel grants will be offered to selected students. Papers presented at the conference will be included in the IEEEExplore Digital Library.

<http://www.spec-ieee.org/>

# SPEC 2022

THE 7<sup>th</sup> IEEE ANNUAL  
SOUTHERN POWER  
ELECTRONICS CONFERENCE

Topics of interest include but are not limited to:

**Devices & Components**

**Power Converters**

**Energy Storage**

**E-mobility**

**Power Supplies**

**Motor Drives and Actuators**

**Other Related Topics**

Prospective authors are invited to submit digests explaining the problem that will be addressed by the paper, author's contribution, how it differs from the closest existing literature, major results that have been obtained and their significance.

Please check <http://www.spec-ieee.org/> for detailed guidelines regarding digest preparation and submission.

Cutting edge researchers in the field will be presenting their works during a four-day program including features tutorials and technical session on theory, analysis, design, testing and advances within the field of power electronics.

Scholarships are offered for students to cover the cost of attending the conference and reduced registration fees apply to delegates from UN least developed countries.

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